

NAME

longrun - Transmeta(TM) Crusoe(TM) LongRun(TM) support

SYNOPSIS

LongRun support is a collection of power saving modes for the Transmeta Crusoe chips, similar in scope to Intel's SpeedStep. The following sysctl(8) MIBs control the different CPU modes:

Name	Type	Changeable	Description
<i>hw.crusoe.longrun</i>	<i>integer</i>	yes	LongRun mode: 0: minimum frequency mode 1: power-saving mode 2: performance mode 3: maximum frequency mode
<i>hw.crusoe.frequency</i>	<i>integer</i>	no	Current frequency (MHz).
<i>hw.crusoe.voltage</i>	<i>integer</i>	no	Current voltage (mV).
<i>hw.crusoe.percentage</i>	<i>integer</i>	no	Processing performance (%).

EXAMPLES

Print the current status:

```
% sysctl hw.crusoe
```

To set LongRun mode to performance oriented variable frequency mode (less power savings):

```
# sysctl hw.crusoe.longrun=2
```

HISTORY

The Transmeta(TM) Crusoe(TM) LongRun(TM) support first appeared in FreeBSD 4.4.

AUTHORS

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