

**NAME**

**pmc\_capabilities**, **pmc\_cpuinfo**, **pmc\_ncpu**, **pmc\_npmc**, **pmc\_pmcinfo**, **pmc\_width** - retrieve information about performance monitoring counters

**LIBRARY**

Performance Counters Library (libpmc, -lpmc)

**SYNOPSIS**

```
#include <pmc.h>
```

*int*

```
pmc_capabilities(pmc_id_t pmc, uint32_t *caps);
```

*int*

```
pmc_cpuinfo(const struct pmc_cpuinfo **cpu_info);
```

*int*

```
pmc_ncpu(void);
```

*int*

```
pmc_npmc(int cpu);
```

*int*

```
pmc_pmcinfo(int cpu, struct pmc_pmcinfo **pmc_info);
```

*int*

```
pmc_width(pmc_id_t pmc, uint32_t *width);
```

**DESCRIPTION**

These functions retrieve information about performance monitoring hardware.

Function **pmc\_capabilities**() retrieves the hardware capabilities of a PMC. Argument *pmc* is a PMC handle obtained by a prior call to **pmc\_allocate**(). The function sets argument *caps* to a bit mask of capabilities supported by the PMC denoted by argument *pmc*. PMC capabilities are described in [pmc\(3\)](#).

Function **pmc\_cpuinfo**() retrieves information about the CPUs in the system. Argument *cpu\_info* will be set to point to an internal structure with information about the system's CPUs. The caller should not free this pointer value. This structure has the following fields:

`pm_cputype`

	Specifies the CPU type.
<code>pm_ncpu</code>	Specifies the number of CPUs in the system.
<code>pm_npmc</code>	Specifies the number of PMC rows per CPU.
<code>pm_nclass</code>	Specifies the number of distinct classes of PMCs in the system.
<code>pm_classes</code>	Contains an array of <i>struct pmc_classinfo</i> descriptors describing the properties of each class of PMCs in the system.

Function **pmc\_ncpu()** is a convenience function that returns the maximum CPU number in the system. On systems that support sparsely numbered CPUs, not all CPUs may be physically present. Applications need to be prepared to deal with nonexistent CPUs.

Function **pmc\_npmc()** is a convenience function that returns the number of PMCs available in the CPU specified by argument *cpu*.

Function **pmc\_pmcinfo()** returns information about the current state of the PMC hardware in the CPU specified by argument *cpu*. The location specified by argument *pmc\_info* is set to point an array of *struct pmc\_info* structures each describing the state of one PMC in the CPU. These structure contain the following fields:

<code>pm_name</code>	A human readable name for the PMC.
<code>pm_class</code>	The PMC class for the PMC.
<code>pm_enabled</code>	Non-zero if the PMC is enabled.
<code>pm_rowdisp</code>	The disposition of the PMC row for this PMC. Row dispositions are documented in <code>hwpmc(4)</code> .
<code>pm_ownerpid</code>	If the hardware is in use, the process id of the owner of the PMC.
<code>pm_mode</code>	The PMC mode as described in <code>pmc(3)</code> .
<code>pm_event</code>	If the hardware is in use, the PMC event being measured.
<code>pm_flags</code>	If the hardware is in use, the flags associated with the PMC.
<code>pm_reloadcount</code>	For sampling PMCs, the reload count associated with the PMC.

Function **pmc\_width()** is used to retrieve the width in bits of the hardware counters associated with a PMC. Argument *pmc* is a PMC handle obtained by a prior call to **pmc\_allocate()**. The function sets the location pointed to by argument *width* to the width of the physical counters associated with PMC *pmc*.

## RETURN VALUES

Functions **pmc\_ncpu()** and **pmc\_npmc()** returns a positive integer if successful; otherwise the value -1 is returned and the global variable *errno* is set to indicate the error.

Functions **pmc\_capabilities()**, **pmc\_cpuinfo()**, **pmc\_pmcinfo()** and **pmc\_width()** return 0 if successful; otherwise the value -1 is returned and the global variable *errno* is set to indicate the error.

**ERRORS**

A call to function **pmc\_capabilities()** may fail with the following errors:

[EINVAL]           The argument to the function was invalid.

Calls to functions **pmc\_cpuinfo()**, **pmc\_ncpu()** and **pmc\_npmc()** may fail with the following errors:

[ENXIO]            A prior call to **pmc\_init()** to initialize the PMC library had failed.

A call to function **pmc\_pmcinfo()** may fail with the following errors:

[EINVAL]           The argument *cpu* was invalid.

[ENXIO]            The argument *cpu* specified a disabled or absent CPU.

A call to function **pmc\_width()** may fail with the following errors:

[EINVAL]           The argument to the function was invalid.

**SEE ALSO**

**pmc(3)**, **pmc\_allocate(3)**, **pmc\_get\_driver\_stats(3)**, **pmc\_name\_of\_capability(3)**,  
**pmc\_name\_of\_class(3)**, **pmc\_name\_of\_cputype(3)**, **pmc\_name\_of\_event(3)**, **pmc\_name\_of\_mode(3)**,  
**hwpmc(4)**